



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group art unit: 2811
Examiner: Ori Nadav

In Re PATENT APPLICATION of:

Applicant(s): Hiroaki NAKANO

Serial No.: 10/756,366

Filing date: January 14, 2004

For: CIRCUIT BOARD FOR MOUNTING A
SEMICONDUCTOR CHIP AND
MANUFACTURING METHOD THEREOF

Atty. ref.: TAI 146

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR
ONE-MONTH
EXTENSION OF
TIME

October 12, 2006

Sir:

It is respectfully requested that the period for replying to the Examiner's Action mailed on June 13, 2006, be extended for one month under the provisions of 37 C.F.R. 1.136(a).

The applicable extension fee of \$120.00 pursuant to 37 C.F.R. 1.17(a) is submitted herewith.

Should this remittance be accidentally missing or insufficient, the Commissioner is hereby authorized to charge the fee to our Deposit Account No. 18-0002, and advise us accordingly.

Respectfully submitted,

October 12, 2006

Date

Robert H. Berdo, Jr. – Reg. No. 38,075
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RHB/vm